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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/773,809	02/05/2004	Kazuya Fujita	60803(49381) 8452		
7590 12/13/2005 DAVID G. CONLIN, ESQ.			EXAMINER		
			LOUIE, WAI SING		
P.O. BOX 5587	ANGELL, LLP 74	ART UNIT	PAPER NUMBER		
BOSTON, MA 02205			2814		
			DATE MAILED: 12/13/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No.	Applicant(s)					
Office Action Summary		10/773,809		FUJITA ET AL.					
		Examiner		Art Unit					
		Wai-Sing Lo		2814					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply									
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
 Responsive to communication(s) filed on <u>22 September 2005</u>. This action is FINAL. 2b)⊠ This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i>, 1935 C.D. 11, 453 O.G. 213. 									
Dispositi	on of Claims								
4) Claim(s) 1-16 and 30-38 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-16 and 30-38 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.									
Application Papers									
9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.									
Priority u	ınder 35 U.S.C. § 119								
12) △ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) △ All b) ☐ Some * c) ☐ None of: 1. △ Certified copies of the priority documents have been received. 2. ☐ Certified copies of the priority documents have been received in Application No 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.									
2) Notice 3) Infor	ot(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-94) mation Disclosure Statement(s) (PTO-1449 or PTO/S er No(s)/Mail Date		4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate	D-152)				

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DETAILED ACTION

The argument in the response to the non-final rejection is persuasive and the non-final rejection of previous office action is withdrawn. A new ground of rejection is as below.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 3-7, 9, 13-15, 30, 32-35, and 37 are rejected under 35 U.S.C. 102(e) as being anticipated by Beyne et al. (US 6,566,745).

With regard to claims 1 and 6, Beyne et al. disclose an image sensor device (col. 5, line 30 to col. 12, line 12 and fig. 1) comprising:

- a solid state imaging device 12 (col. 5, line 65 to col. 6, line 1) having an effective optical sensitive area (pixels) in one surface (col. 5, lines 60-61 and fig. 1);
- a light-transparent cover 11 arranged opposite to the effective pixel region and having planar dimension smaller than those of the solid state imaging device 12 (fig. 1);

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 an adhering section 15 for adhering the solid-state image pickup device 12 and light-transparent cover 11 (col. 6, lines 21-24).

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With regard to claims 3-5, 9, 13-15 Beyne et al. disclose a space is formed between the effective pixel region 12 and the light-transparent cover 11 and where the adhering section 15 is formed outside the effective pixel region 12 and seal the outer periphery of the space (col. 6, lines 21-24 and fig. 1).

With regard to claim 7, Beyne et al. disclose the light-transparent plate 61 is divided so as to form light-transparent covers each have planar dimensions smaller than those of the solid state image pickup device (fig. 6a to fig. 6d).

With regard to claims 30, 32, and 37, Beyne et al. disclose an image sensor package device (col. 5, line 31 to col. 12, line 24 and fig. 6) comprising:

- A wiring board 66 on which wiring is formed (col. 8, lines 5-45 and fig. 6 and 9);
- An image processor 62 adhered to the wiring board 66 and electrically connected to the wiring (col. 8, lines 5-45 and fig. 6);
- A solid state (CMOS) imaging device in which a light transparent cover 61
 having planar dimensions smaller than those of a solid state image pickup device
 62 is attached opposite to the effective pixel region of the solid state image pickup
 device, and which is adhered to the image processor 62 and electrically connected
 to the wiring (fig. 6);
- A sealing section 65 for resin sealing the wiring board 66 (col. 8, lines 24-30), the image processor and the solid state image device 62 in a state that the surface of the light transparent cover 61 is exposed (fig. 5i);

• An optical path defining unit 613 arranged opposite to the solid state-imaging device 62 and defining an optical path to the solid-state device (fig. 6f).

With regard to claim 33, Beyne et al. disclose the image processor 62 adhere the wiring board 66 and the external terminal connected to the wiring (fig. 6g and 9). Inherently, the external terminals are reverse to the wiring on the surface of the wiring board that the image processor adhered to.

With regard to claim 34, Beyne et al. disclose the external terminal has a protruding shape in form of a solder ball (fig. 6h).

With regard to claim 35, Beyne et al. disclose a wiring board 66 on which wiring is formed and where the external terminal 67 of the module component wiring board is connected to the wiring of the wiring board (fig. 6h and 9).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2, 8, 10-12, 31, 36, and 38 are rejected under 35 U.S.C. 103(a) as being unpatentable over Beyne et al. (US 6,566,745) in view of Hashimoto (US Pub. 2003/0123779).

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With regard to claims 2, 8, 10, and 12, Beyne et al. do not disclose the adhesive is a photosensitive adhesive. However, Hashimoto discloses using ultraviolet light adhesive (Hashimoto paragraph [0089]). Hashimoto teaches using UV-cured adhesive could temporarily cured for easy handling the device (Hashimoto paragraph [0089]). Therefore, it would have been obvious to one of ordinary skill in the art to modify Beyne's device with the teaching of Hashimoto to use the photosensitive adhesive in the adhering section in order to handle the temporarily cured device with easy.

With regard to claim 11, 31, 36, and 38, in addition to the limitations disclosed in claims 1 and 6 above, Beyne et al. modified by Hashimoto also disclose:

 Beyne et al. modified by Hashimoto disclose a lens 78 inside a lens retainer arranged opposite to the light transparent cover of the solid state-imaging device (Hashimoto paragraph [0111] and fig. 9).

Response to Arguments

Applicant's arguments filed 9/22/05 have been fully considered but they are not persuasive.

Applicant argues that there is no "optical path defining unit" in Beyne et al.
 Please see above rejection to claims 30, 32, and 37.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Wai-Sing Louie whose telephone number is (571) 272-1709. The examiner can normally be reached on 7:30 AM to 4:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Wai-Sing Louie Patent Examiner

Wsl December 9, 2005.